IN THE UNITED STATES PATENT AND TRADEMARK OFFICE	1F	
RESPONSE UNDE	ER RULE 116	
The Patent Application of MAY OR 2006 W C# M# KIMURA et al MAY OR 2006 W C# M#	ROCEDURES	
KIMURA et al		
Serial No. 10/670,194 Examiner: Im, Junghwa		
Filed: September 26, 2003 Date: May 8, 2006		
Title: CHIP-STACK SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THE SAME		
Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450		
Sir:		
RESPONSE/AMENDMENT/LETTER This is a response/amendment/letter in the above-identified application and includes an attachment which is hereby incorporated by reference and the signature below serves as the signature to the attachment in the absence of any other signature thereon. Image: Correspondence Address Indication Form Attached.		
Fees are attached as calculated below:		
Total effective claims after amendment14minus highest numberpreviously paid for20(at least 20) =0x \$50.00\$0.00 (1202)/\$0.00 (2202)	\$	
Independent claims after amendment2minus highest numberpreviously paid for3(at least 3) =0x \$200.00\$0.00 (1201)/\$0.00 (2201)	\$	
If proper multiple dependent claims now added for first time, (ignore improper); add \$360.00 (1203)/\$180.00 (2203)	\$	
Petition is hereby made to extend the current due date so as to cover the filing date of this paper and attachment(s) Two Month Extensions \$450.00 (1252)/\$225.00 (2252) Three Month Extensions \$1020.00 (1253/\$510.00 (2253) Four Month Extensions \$1590.00 (1254/\$795.00 (2254) Five Month Extensions \$2160.00 (1255/\$1080.00 (2255)	\$	
Terminal disclaimer enclosed, add \$130.00 (1814)/ \$65.00 (2814)	\$	
Applicant claims "small entity" status.		
Rule 56 Information Disclosure Statement Filing Fee \$180.00 (1806)	\$	
Assignment Recording Fee \$40.00 (8021)	\$	
Other:	\$	
TOTAL FEE ENCLOSED	\$ 0.00	

The Commissioner is hereby authorized to charge any <u>deficiency</u>, or credit any overpayment, in the fee(s) filed, or asserted to be filed, or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Account No. 14-1140. A <u>duplicate</u> copy of this sheet is attached.

901 North Glebe Road, 11th Floor Arlington, Virginia 22203-1808 Telephone: (703) 816-4000 Facsimile: (703) 816-4100 JAR:caj	NIXON & VANDERHYE P.C By Atty: Joseph A. Rhae, Reg. No. 37,515 Signature:



In re Patent Application of

KIMURA et al

Atty. Ref.: 1035-471; Confirmation No. 4479

Appl. No. 10/670,194 TC/A.U. 2811

Filed: September 26, 2003 Examiner: Im, Junghwa

For: CHIP-STACK SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THE SAME

* * * * * * * * * *

May 8, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

AMENDMENT AFTER FINAL

Responsive to the Official Action dated February 9, 2006, please amend the

above-identified application as follows: